# Low On-Resistance, 5V Wide-Bandwidth 5-port, 2:1 Mux/DeMux VideoSwitch 

## Features

- High-performance, low-cost solution to switch between video sources
- Wide bandwidth: 550 MHz
- Low On-Resistance: $5 \Omega$
- Low crosstalk at $10 \mathrm{MHz}:-90 \mathrm{~dB}$
- Ultra-low quiescent power ( $0.1 \mu \mathrm{~A}$ typical)
- Single supply operation: +5.0 V
- Fast switching: 10 ns
- Packaging (Pb-free \& Green available):
- 24-pin 150-mil wide plastic QSOP (Q)


## Applications

- Projection TV and LCD TV
- Video consumer applications
- Analog video signal processing


## Description

Pericom Semiconductor's PI5V512 is a true bidirectional 5-Port 2:1 multiplexer/demultiplexer with $\mathrm{Hi}-\mathrm{Z}$ outputs that is recommended for both RGB and composite video switching applications. With the additional 2 ports, vertical and horizontal synchronous signals can be switched in addition to switching the RGB and composite signals between different components, such as DVD, VCR, PCs and etc. The video switch can be driven from a current output RAMDAC or voltage output composite video source.
Low On-Resistance, Low Crosstalk, Low OFF Isolation and wide bandwidth features make it ideal for video and other applications. The device also has exceptional high current capability which is far greater than most analog switches offered today. A single 5 V supply is all that is required for operation. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. The PI5V512 offers a high-performance ( 550 MHz ), low-cost solution to switch between video sources.

## Pin Configuration

$\square$

## Truth Table

| $\overline{\mathbf{E N}}$ | $\mathbf{S}$ | ON Switch |
| :---: | :---: | :--- |
| 0 | 0 | ${ }_{\mathrm{I}} \mathrm{A}_{0},{ }_{\mathrm{I}} \mathrm{B}_{0},{ }_{\mathrm{I}} \mathrm{C}_{0},{ }_{\mathrm{I}} \mathrm{D}_{0},{ }_{\mathrm{I}} \mathrm{E}_{0}$ |
| 0 | 1 | ${ }_{\mathrm{I}} \mathrm{A}_{1},{ }_{\mathrm{I}} \mathrm{B}_{1},{ }_{\mathrm{I}} \mathrm{C}_{1},{ }_{\mathrm{I}} \mathrm{D}_{1},{ }_{\mathrm{I}} \mathrm{E}_{1}$ |
| 1 | X | Disabled |

## Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)
Storage Temperature .................................................... $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$
Ambient Temperature with Power Applied ....................... $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
Supply Voltage to Ground Potential (Inputs \& VCC Only)... -0.5 V to +7.0 V
Supply Voltage to Ground Potential (Outputs \& D/O Only) -0.5 V to +7.0 V
DC Input Voltage .................................................................. -0.5 V to +7.0 V
DC Output Current............................................................................... 120 mA
Power Dissipation ..................................................................................... 0.5 W

## Pin Description

| Pin Name | Description |
| :--- | :--- |
| ${ }_{\mathrm{I}} \mathrm{A}_{0},{ }_{\mathrm{I}} \mathrm{B}_{0},{ }_{\mathrm{I}} \mathrm{C}_{0},{ }_{\mathrm{I}} \mathrm{D}_{0},{ }_{\mathrm{I}} \mathrm{E}_{\mathrm{Q}},{ }_{\mathrm{I}} \mathrm{A}_{1},{ }_{\mathrm{I}} \mathrm{B}_{1},{ }_{\mathrm{I}} \mathrm{C}_{1},{ }_{\mathrm{I}} \mathrm{D}_{1},{ }_{\mathrm{I}} \mathrm{E}_{1}$ | Analog Video I/O |
| S | Select Input |
| $\overline{\mathrm{EN}}$ | Enable |
| $\mathrm{Y}_{\mathrm{A}}, \mathrm{Y}_{\mathrm{B}}, \mathrm{Y}_{\mathrm{C}}, \mathrm{D}_{\mathrm{E}}, \mathrm{Y}_{\mathrm{D}}, \mathrm{E}_{\mathrm{E}}$ | Analog Video I/O |
| GND | Ground |
| $\mathrm{V}_{\mathrm{CC}}$ | Power |

DC Electrical Characteristics (Over the Operating Range, $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V} \pm 5 \%$ )

| Parameters | Description | Test Conditions ${ }^{(1)}$ | Min. | Typ. ${ }^{(2)}$ | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Vanalog | Analog Signal Range |  | 0 |  | 2.0 | V |
| $\mathrm{V}_{\mathrm{IH}}$ | Input HIGH Voltage | Guaranteed Logic HIGH Level | 2.0 |  |  |  |
| $\mathrm{V}_{\text {IL }}$ | Input LOW Voltage | Guaranteed Logic LOW Level | -0.5 |  | 0.8 |  |
| $\mathrm{I}_{\text {IH }}$ | Input HIGH Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=\mathrm{V}_{\text {CC }}$ |  |  | $\pm 1$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\text {IL }}$ | Input LOW Current | $\mathrm{V}_{\mathrm{CC}}=$ Max., $\mathrm{V}_{\text {IN }}=$ GND |  |  | $\pm 1$ |  |
| $\mathrm{I}_{\mathrm{O}}$ | Analog Output Leakage Current | $0 \leq \mathrm{I} 0$, I1 or $\mathrm{D} \leq \mathrm{V}_{\mathrm{CC}}$, Switch OFF |  |  | $\pm 1$ |  |
| $\mathrm{V}_{\text {IK }}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=$ Min., $\mathrm{I}_{\mathrm{IN}}=-18 \mathrm{~mA}$ | -0.7 | -1.2 | V |  |
| $\mathrm{V}_{\mathrm{H}}$ | Input Hysteresis at Control Pins |  |  | 150 |  | mV |
| $\mathrm{R}_{\mathrm{ON}}$ | Switch On-Resistance ${ }^{(3)}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=1.0 \mathrm{~V}, \\ & \mathrm{I}_{\mathrm{ON}}=13 \mathrm{~mA} \end{aligned}$ |  | 5 | 8 | $\Omega$ |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=\mathrm{Min} ., \mathrm{V}_{\mathrm{IN}}=2.0 \mathrm{~V}, \\ & \mathrm{I}_{\mathrm{ON}}=26 \mathrm{~mA} \end{aligned}$ |  | 7 | 12 |  |

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $\mathrm{Vcc}=5.0 \mathrm{~V}, \mathrm{TA}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
3. Measured by the voltage drop between IO, I1, and D I/O pins at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the $\mathrm{I} 0, \mathrm{I} 1$, and D I/O pins.

Dynamic Characteristics (Over the Operating Range, $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V} \pm 5 \%$ )

| Parameters | Description | Test Conditions | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| ton | Turn On Time | $\mathrm{R}_{\mathrm{L}}=75 \Omega, \mathrm{C}_{\mathrm{L}}=20 \mathrm{pF}$, See Fig. 1 |  | 2.5 | 5 | ns |
| toff | Turn Off Time | $\mathrm{R}_{\mathrm{L}}=75 \Omega, \mathrm{C}_{\mathrm{L}}=20 \mathrm{pF}$, See Fig. 1 |  | 1.1 | 5 |  |
| $\mathrm{B}_{\mathrm{W}}{ }^{(1)}$ | -3 dB Bandwidth | See Fig. 2, $\mathrm{C}_{\mathrm{L}}=0 \mathrm{pF}$ | 100 | 550 |  | MHz |
| $\mathrm{X}_{\text {TALK }}{ }^{(1)}$ | Crosstalk | 10 MHz , See Fig. 2 |  | -90 |  | dB |
| $\mathrm{C}_{\text {IN }}{ }^{(1)}$ | Input/Enable Capacitance | $\mathrm{V}_{\text {IN }}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  |  | 6 | pF |
| $\mathrm{COFF}^{(1)}$ | Capacitance, Switch Off | $\mathrm{V}_{\mathrm{IN}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  |  | 6 |  |
| $\mathrm{CON}^{(1)}$ | Capacitance, Switch On | $\mathrm{V}_{\text {IN }}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  |  | 11 |  |
| OIRR | Off Isolation | 10 MHz , See Fig 2 |  | -60 |  | dB |

Notes:

1. This parameter is determined by device characterization but is not production tested.

## Power Supply Characteristics

| Parameters | Description | Test Conditions ${ }^{(\mathbf{1})}$ |  | Min. | Typ ${ }^{(2)}$ | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\mathrm{CC}}$ | Quiescent Power Supply Current | $\mathrm{V}_{\mathrm{CC}}=$ Max. | $\mathrm{IN}=\mathrm{GND}$ or $\mathrm{V}_{\mathrm{CC}}$ |  | 0.1 | 3.0 | $\mu \mathrm{A}$ |
| $\Delta_{\text {ICC }}$ | Supply Current per Input @ TTL HIGH | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max}$. | $\mathrm{IN}=3.4 \mathrm{~V}^{(3)}$ |  |  | 2.5 | mA |
| $\mathrm{I}_{\text {CCD }}$ | Supply Current per Input per MHz ${ }^{(4)}$ | $\mathrm{V}_{\mathrm{CC}}=\mathrm{Max} ., \mathrm{S} 1, \mathrm{~S} 2 \text { and } \mathrm{D}$ <br> Pins Open $\overline{\mathrm{EN}}=$ GND <br> Control Input Toggling 50\% <br> Duty Cycle |  |  |  | 0.25 | $\begin{aligned} & \mathrm{mA} / \\ & \mathrm{MHz} \end{aligned}$ |

## Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V},+25^{\circ} \mathrm{C}$ ambient.
3. Per TTL driven input ( $\mathrm{V}_{\mathrm{IN}}=3.4 \mathrm{~V}$, control inputs only); $\mathrm{I} 0, \mathrm{I} 1$, and Y pins do not contribute to Icc.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The I0, I1, and Y I/O pins generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

## Definitions

| Symbol | Description |
| :---: | :---: |
| $\mathrm{R}_{\text {ON }}$ | Resistance between source and drain with switch in the ON state. |
| $\mathrm{I}_{\mathrm{O}}$ | Output leakage current measured at $\mathrm{I} 0, \mathrm{II}$, and Y with the switch OFF. |
| $\mathrm{V}_{\text {IN }}$ | Digital voltage at the S pin that selects between I0 and I1 analog inputs. |
| $\mathrm{V}_{\mathrm{EN}}$ | A voltage that ENABLES the chip. |
| $\mathrm{C}_{\text {IN }}$ | Capacitance at the digital inputs. |
| Coff | Capacitance at analog I/O (I0, I1, and Y) with switch OFF. |
| CON | Capacitance at analog I/O ( $10, \mathrm{Il}$, and Y) with switch ON. |
| $\mathrm{V}_{\mathrm{IH}}$ | Minimum input voltage for logic HIGH. |
| $\mathrm{V}_{\text {IL }}$ | Minimum input voltage for logic LOW. |
| $\mathrm{I}_{\text {IH }}\left(\mathrm{I}_{\text {IL }}\right)$ | Input current of the digital input. |
| ton | Propagation delay measured between $50 \%$ of the digital input to $90 \%$ of the analog output when switch is turned ON. The peak analog voltage is 0.714 V . |
| toff | Propagation delay measured between $50 \%$ of the digital input to $90 \%$ of the analog output when switch is turned OFF. The peak analog voltage is 0.714 V . |
| $\mathrm{B}_{\mathrm{W}}$ | Frequency response of the switch in the ON state measured at 3dB down. |
| $\mathrm{X}_{\text {TALK }}$ | Unwanted signal coupled from channel to channel, measured in $-\mathrm{dB} . \mathrm{X}_{\text {TALK }}=20$ LOG $\mathrm{V}_{\text {OUT }} / \mathrm{V}_{\text {IN }}$. This is nonadjacent crosstalk. |
| $\mathrm{O}_{\text {IRR }}$ | Off isolation is the resistance (measured in -dB ) between the input and output with the switch off (NO). |



Figure 1. Switching Time


Figure 2. Gain/Phase, Crosstalk, Off-Isolation


Figure 3. Bandwidth @ -3dB


Figure 4. Crosstalk @ $\mathbf{2 5}^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=\mathbf{4 . 7 5 V}$


Figure 5. Off Isolation, $\mathbf{2 5}^{\circ} \mathrm{C} \mathrm{V}_{\mathrm{CC}}=4.75$


Figure 6. Bandwidth vs Capacitance

## Packaging Mechanical: 24-pin QSOP (Q)


$\frac{X . X X}{X X X}$ DENOTES DIMENSIONS
X.XX IN MILLIMETERS

## Ordering Information

| Ordering Code | Package Code | Package Description |
| :---: | :---: | :--- |
| PI5V512Q | Q | 24-pin 150-mil wide plastic QSOP |
| PI5V512QE | Q | Pb-free \& Green, 24-pin 150-mil wide plastic QSOP |

Notes:

1. Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
